

FIG. 1

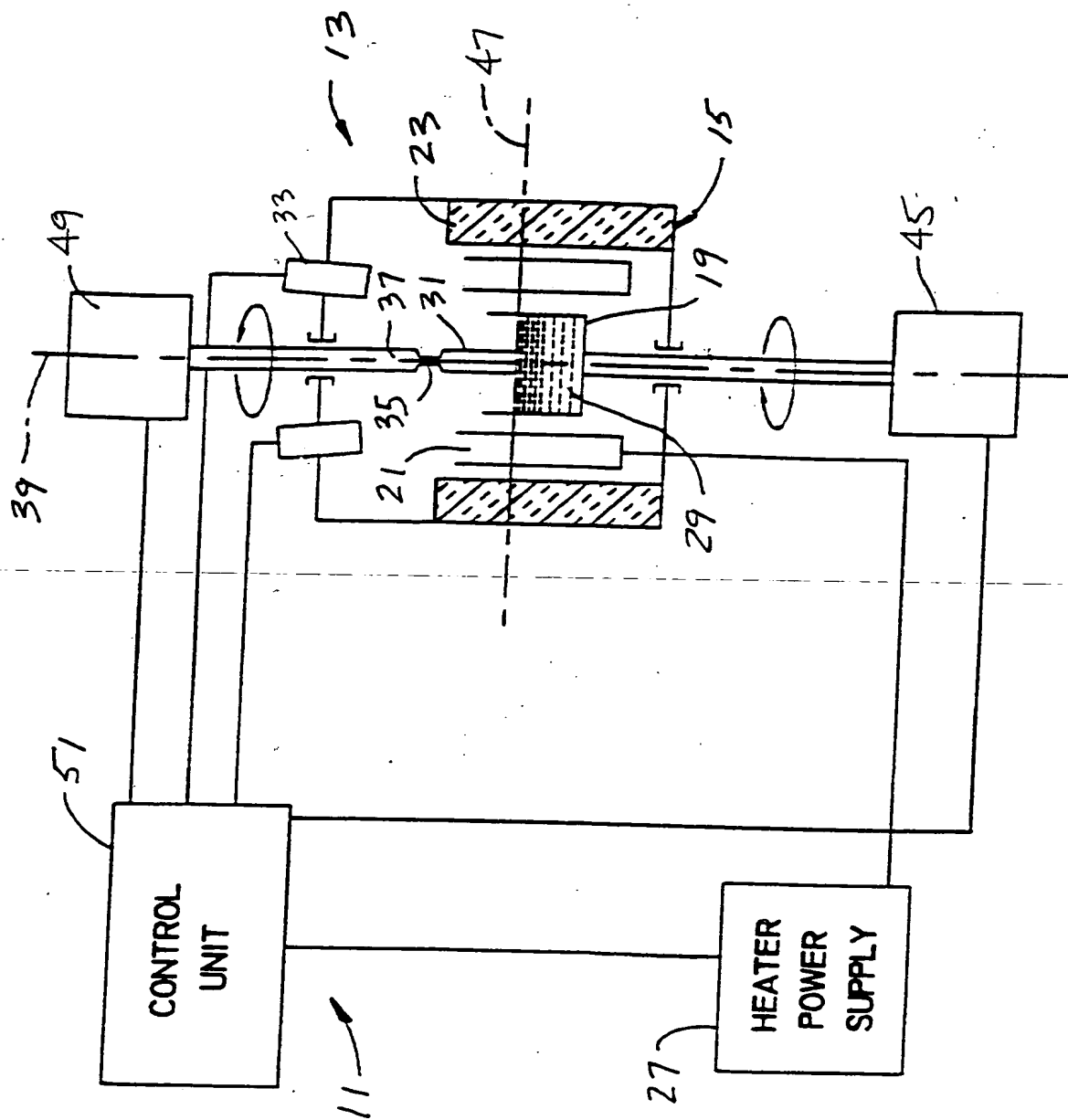
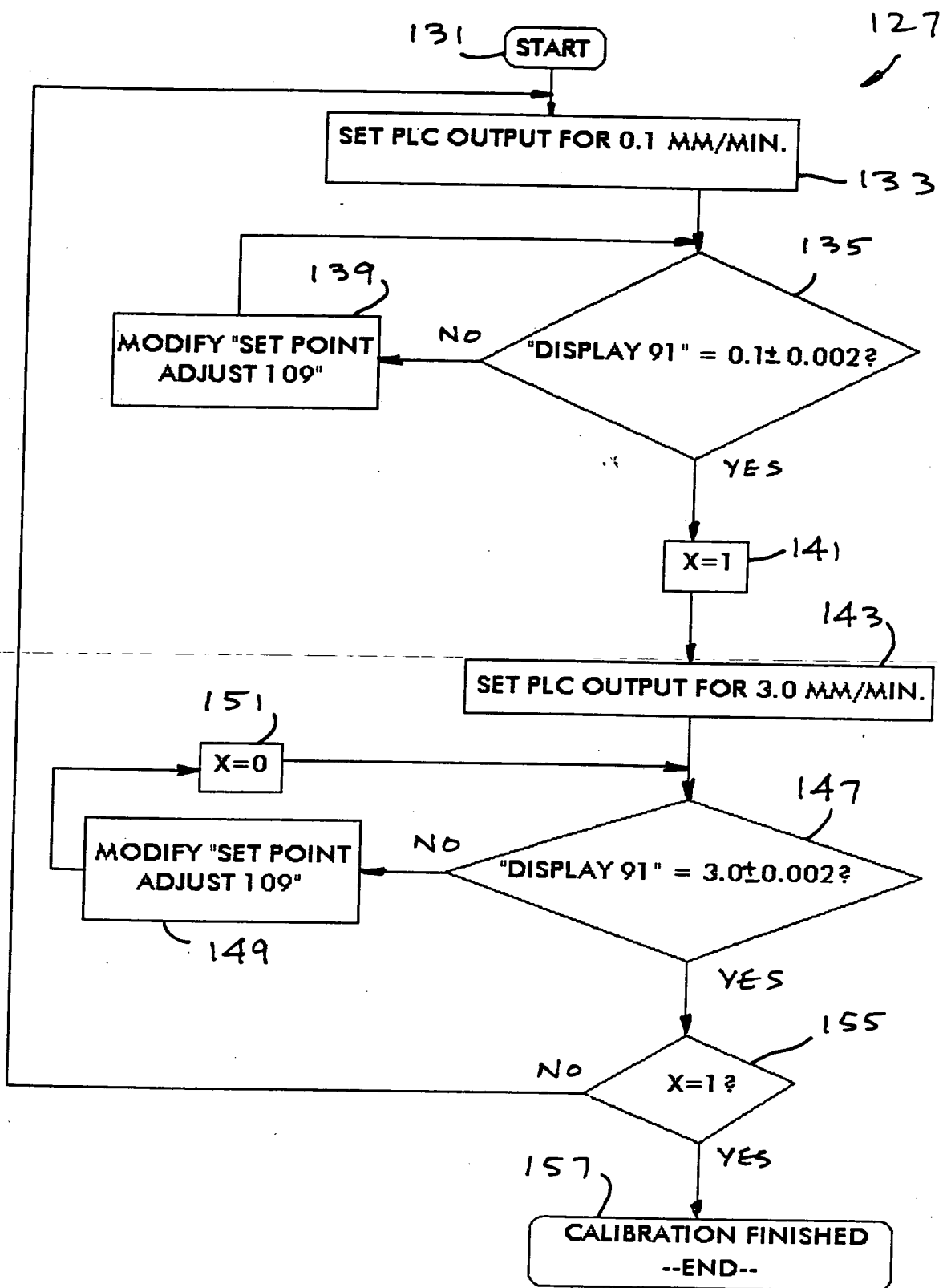


FIG. 3



000720 01E20560

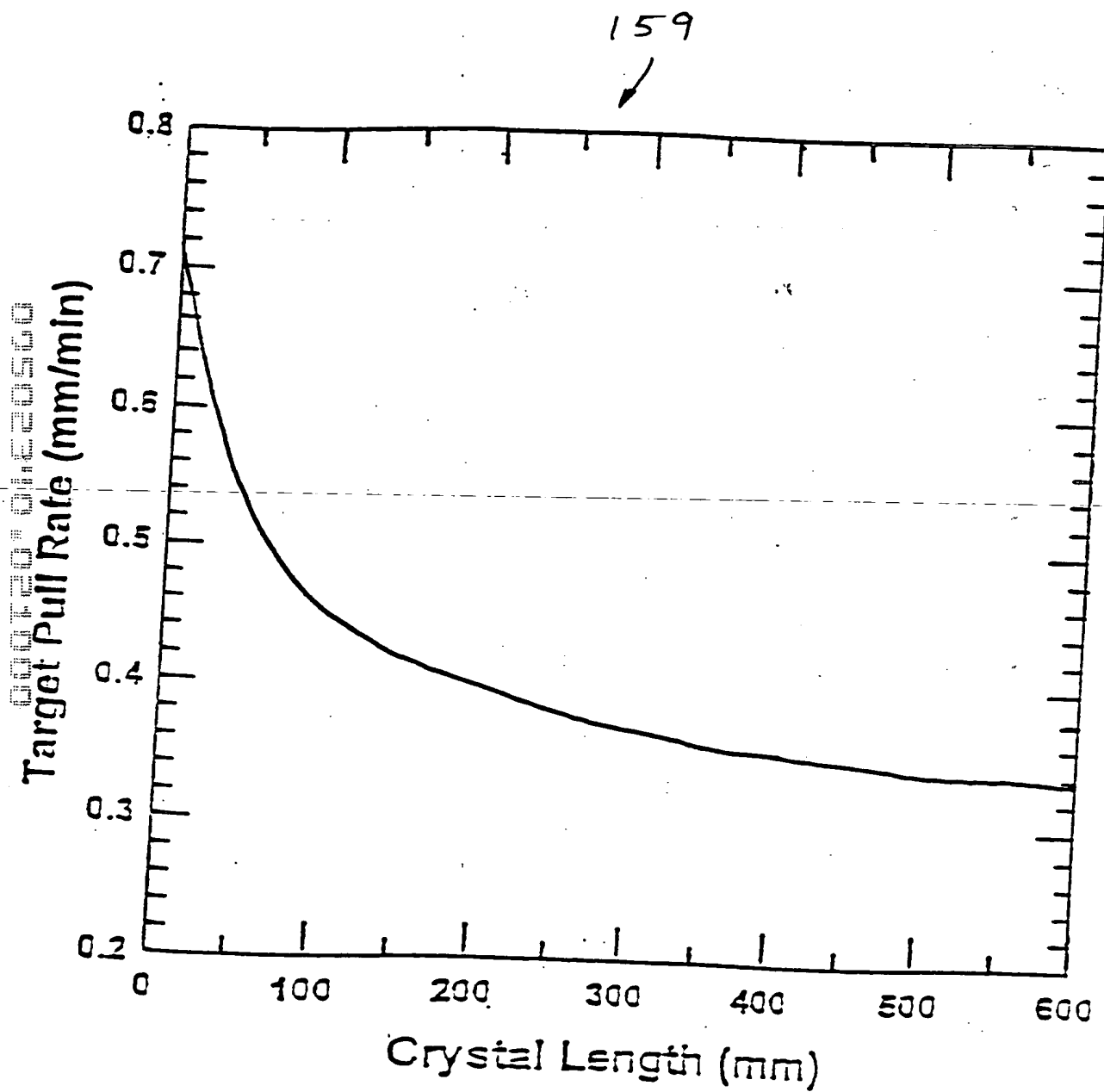


FIG. 4

[illegible]

Fig. 5

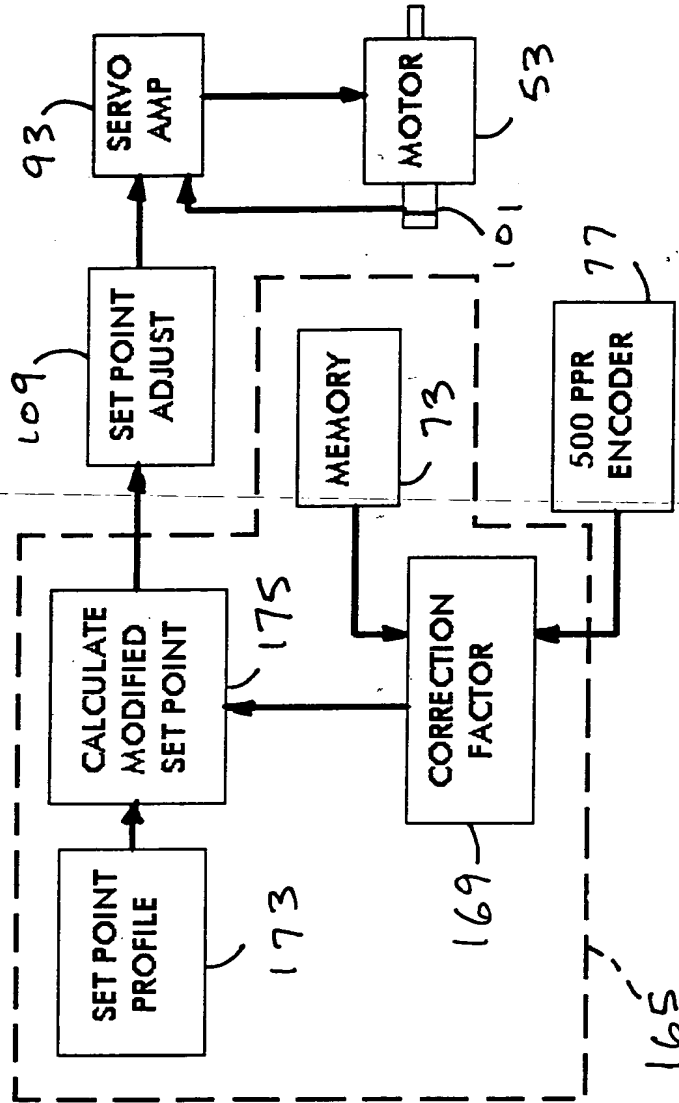


FIG. 6A

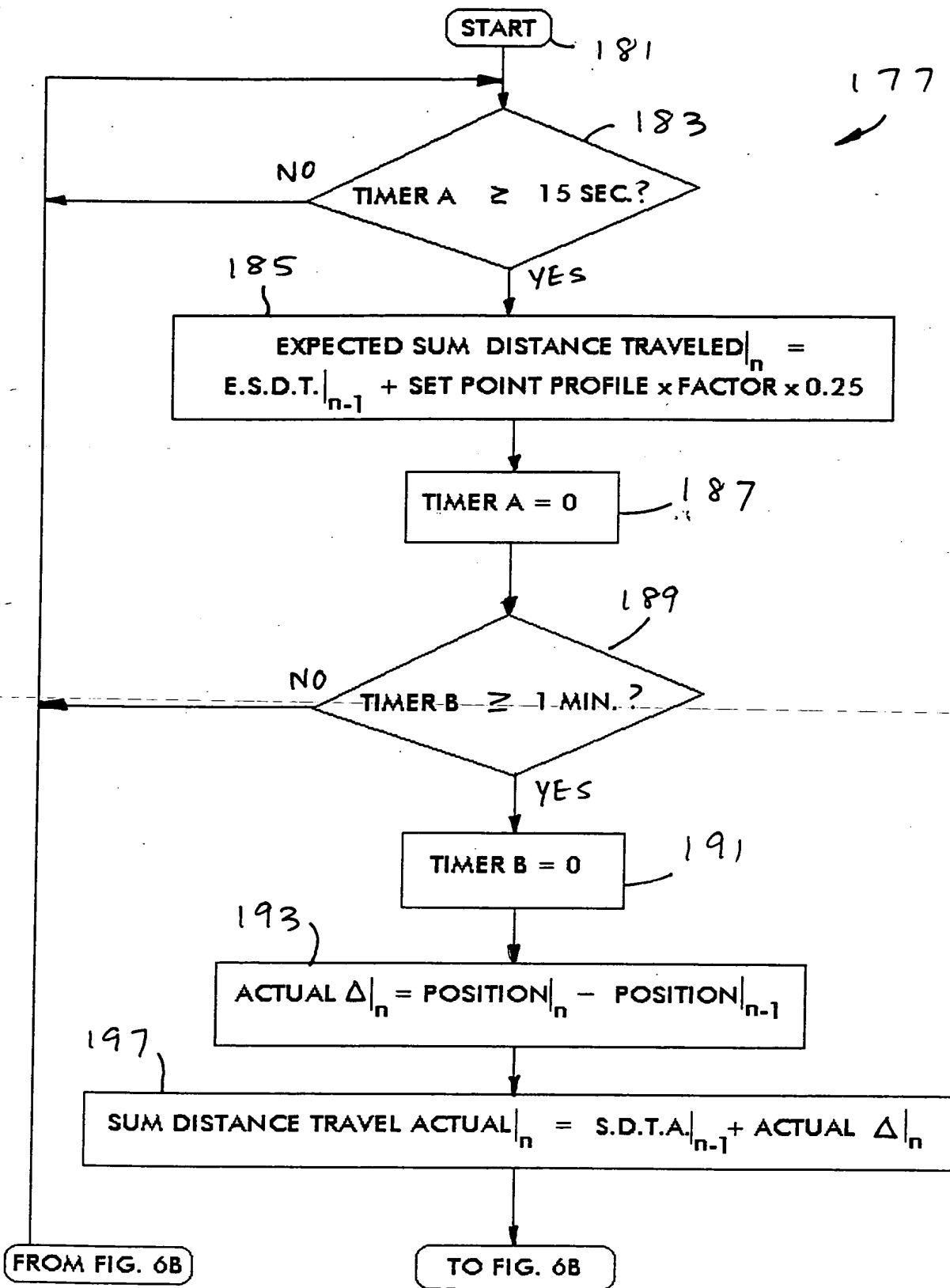
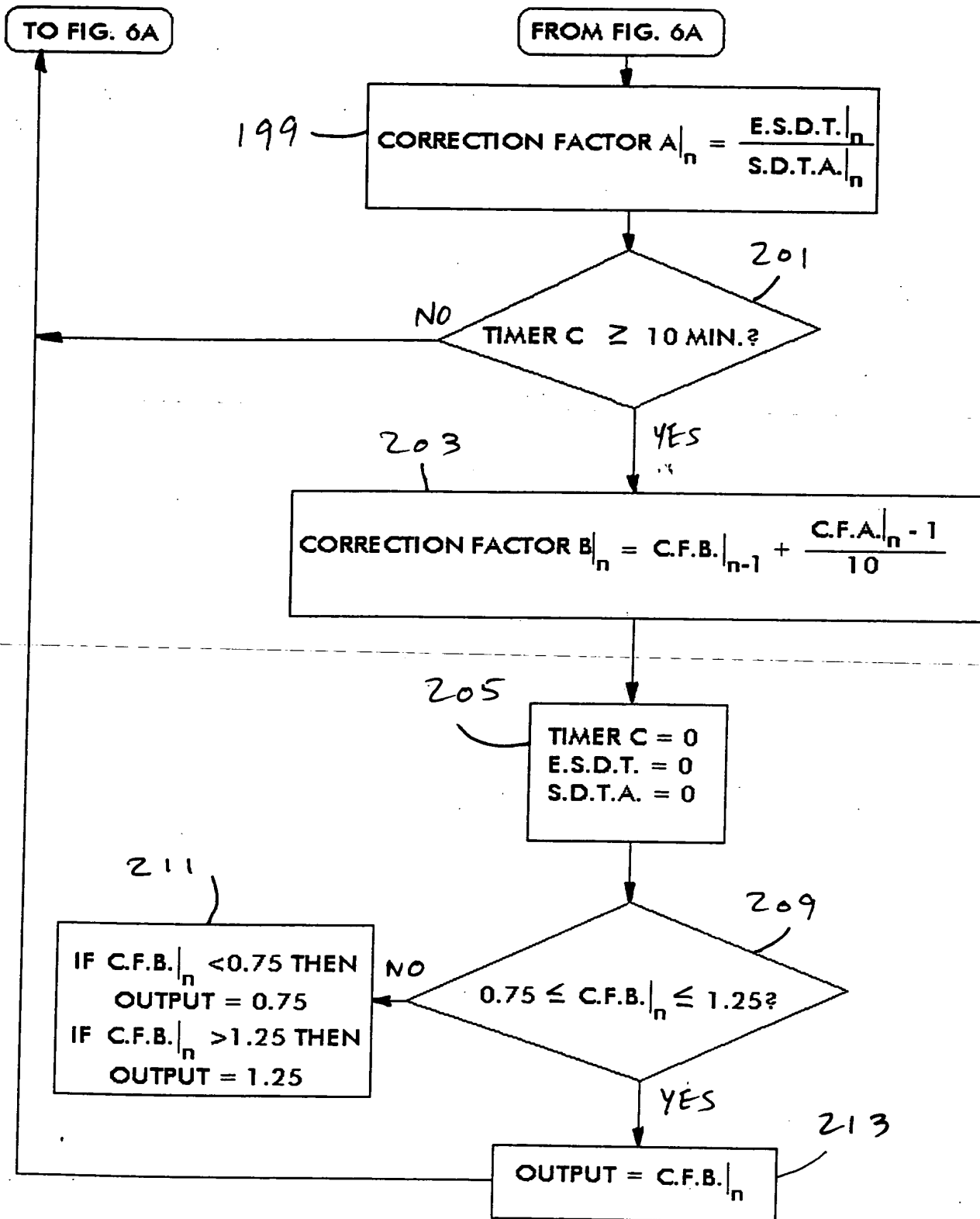


FIG. 6B



Diameter Control Loop -- Seed Lift

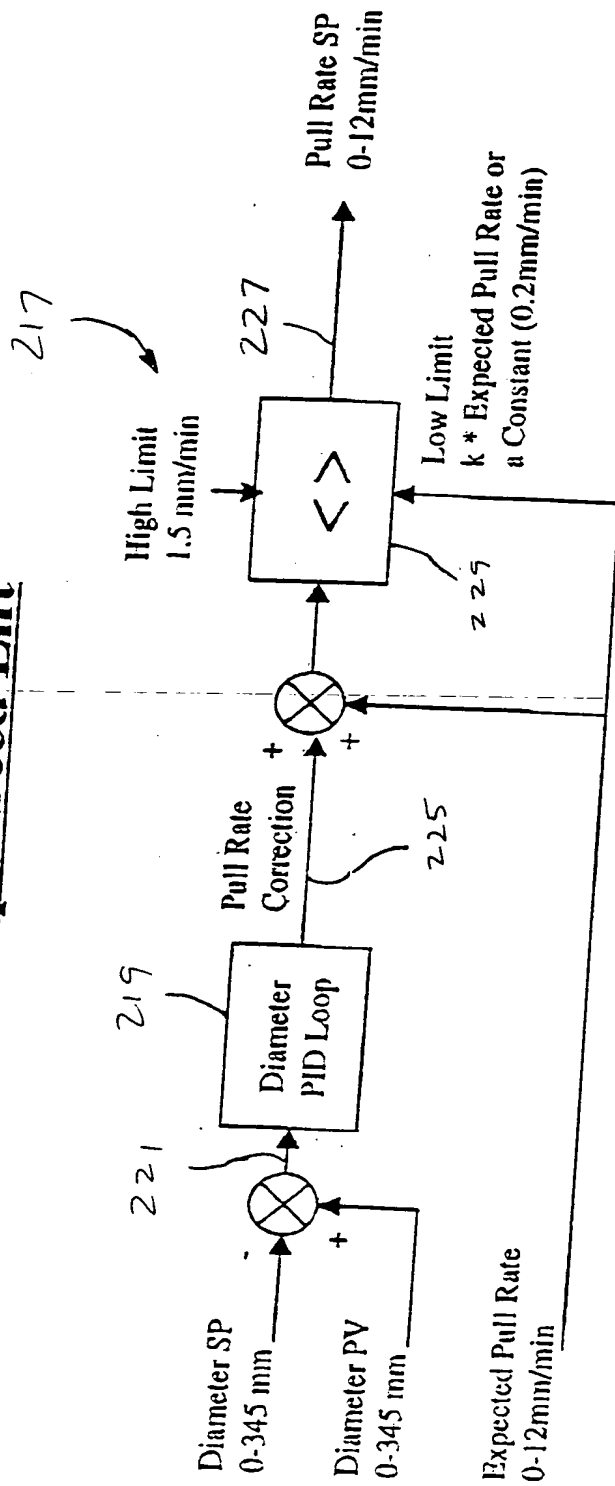


FIG. 7

PRIOR ART

Cascaded Temperature Control Loops

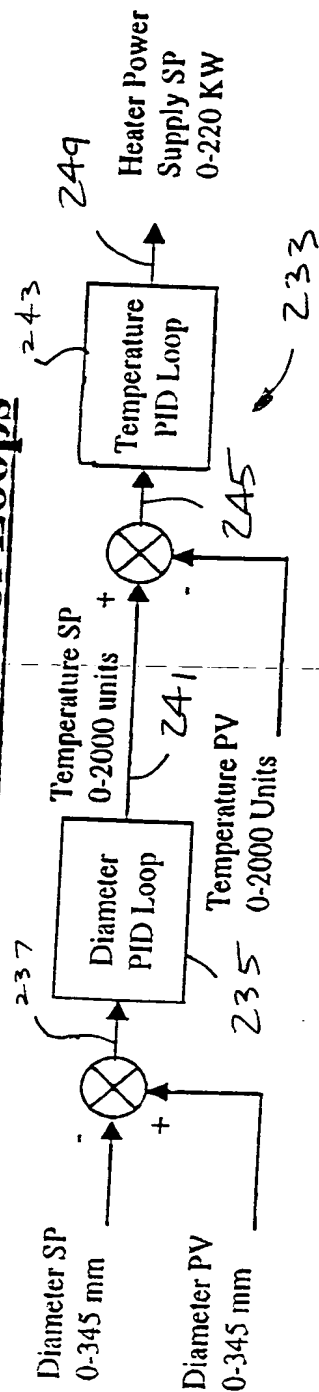


FIG. 9 PRIOR ART

Power Control Loop

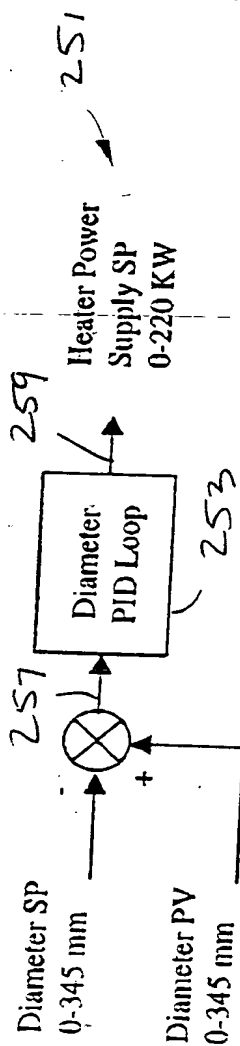


FIG. 10

Power Control Loop With Process Model

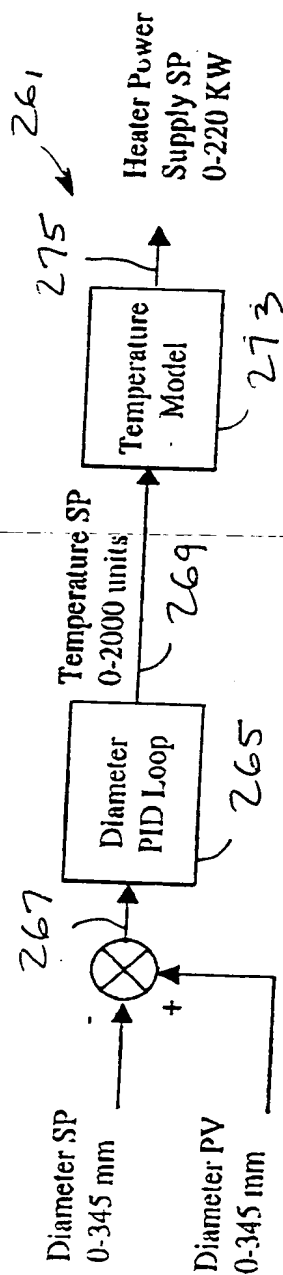
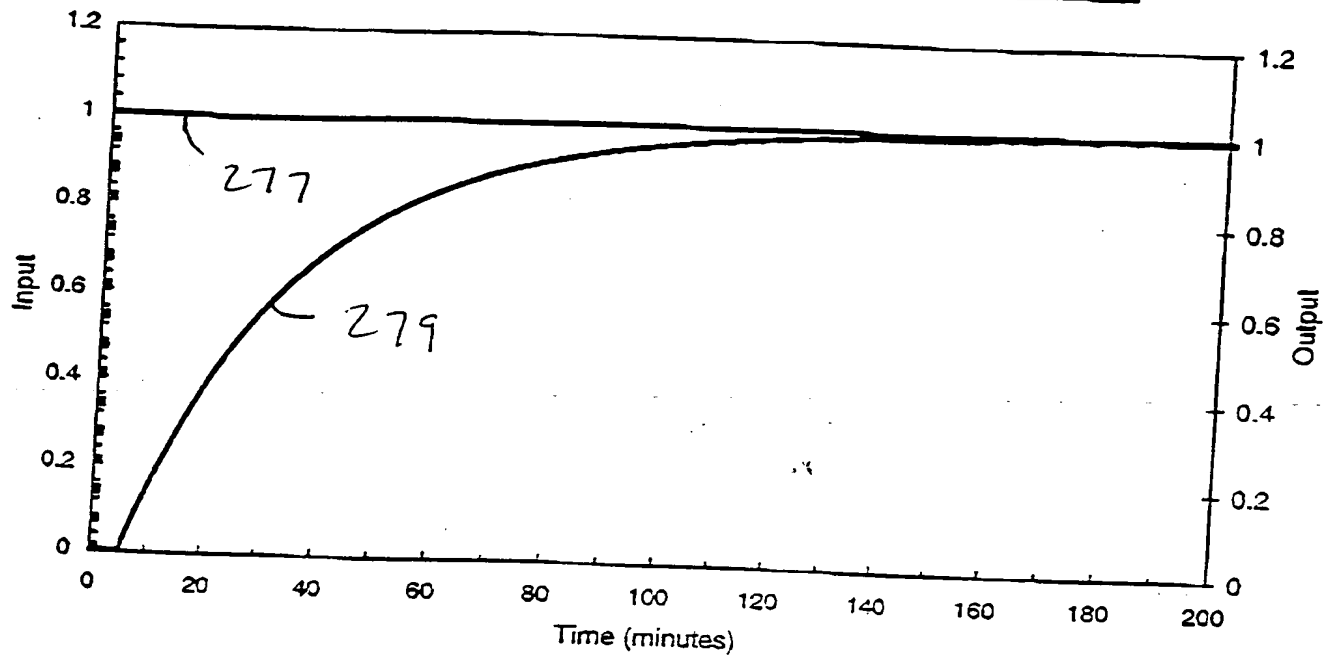


FIG. 11 PRIOR ART

First-Order Lag Function Response
(Unity Step Input at $t=1$ Without Pulse, $\tau = 30$ minutes, $T_d = 5$ minutes)



First-Order Lag Function Response
(Unity Step Input at $t=1$ With 3.16 Minute, 10x Pulse, $\tau = 30$ minutes, $T_d = 5$ minutes)

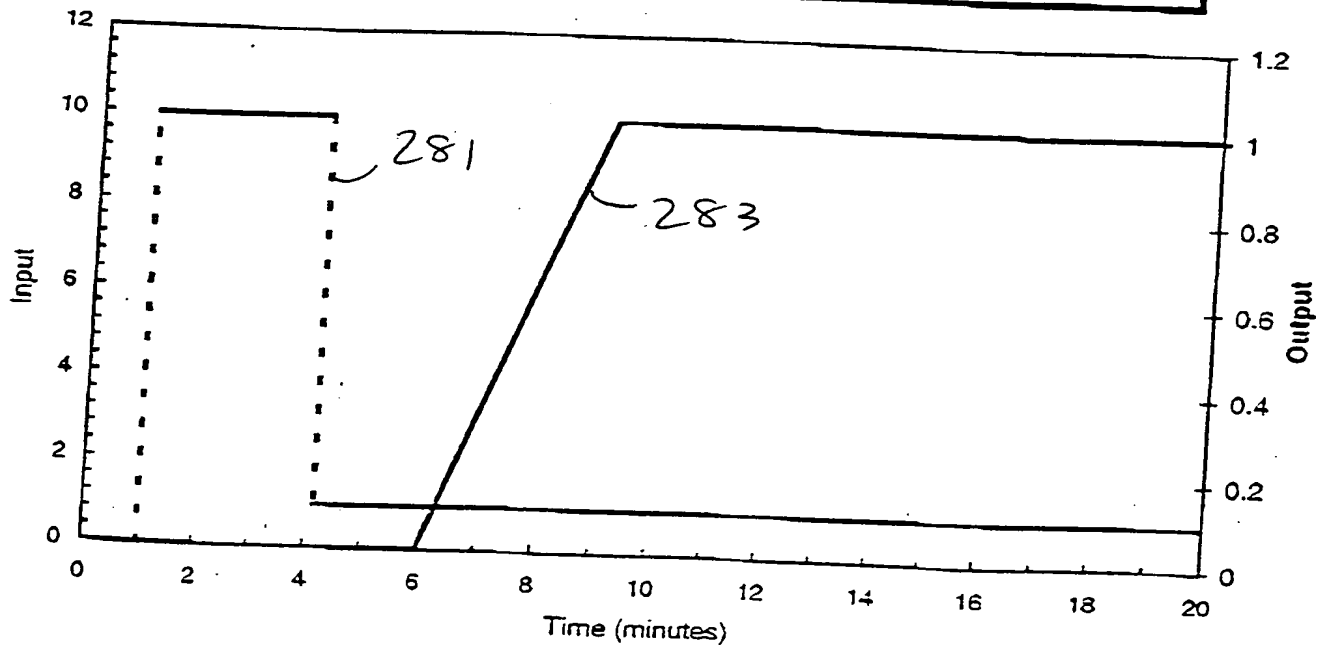


FIG. 12

FIG. 13

